



SPP2095

P-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPP2095 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application, such as DC/DC converter and Desktop computer power management.

The package is universally preferred for commercial industrial surface mount applications

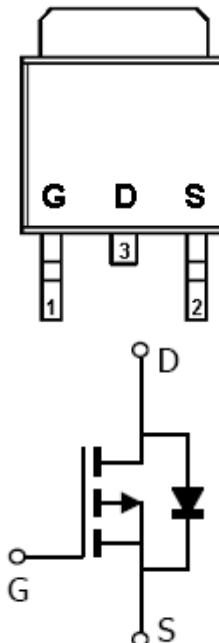
APPLICATIONS

- Power Management in Desktop Computer
- DC/DC Converter
- LCD Display inverter

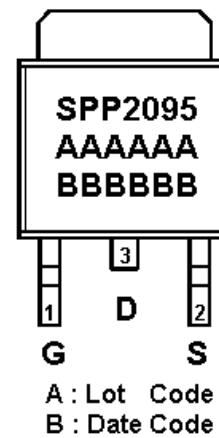
FEATURES

- ◆ -20V/-6.0A,R_{DS(ON)}= 65mΩ@V_{GS}=-4.5V
- ◆ -20V/-3.6A,R_{DS(ON)}= 85mΩ@V_{GS}=-2.5V
- ◆ -20V/-2.0A,R_{DS(ON)}=105mΩ@V_{GS}=-1.8V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-252-2L package design

PIN CONFIGURATION (TO-252-2L)



PART MARKING





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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPP2095T252RG	TO-252-2L	SPP2095
SPP2095T252RGB	TO-252-2L	SPP2095

- ※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)
- ※ SPP2095T252RG : Tape Reel ; Pb – Free
- ※ SPP2095T252RGB : Tape Reel ; Pb – Free ; Halogen -Free

ABSOLUTLE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	-20	V
Gate –Source Voltage	V _{GSS}	±12	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	ID	A
	T _A =70°C		
Pulsed Drain Current	I _{DM}	-20	A
Continuous Source Current(Diode Conduction)	I _S	-2.3	A
Power Dissipation	T _A =25°C	P _D	W
	T _A =70°C		
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	105	°C/W



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ELECTRICAL CHARACTERISTICS

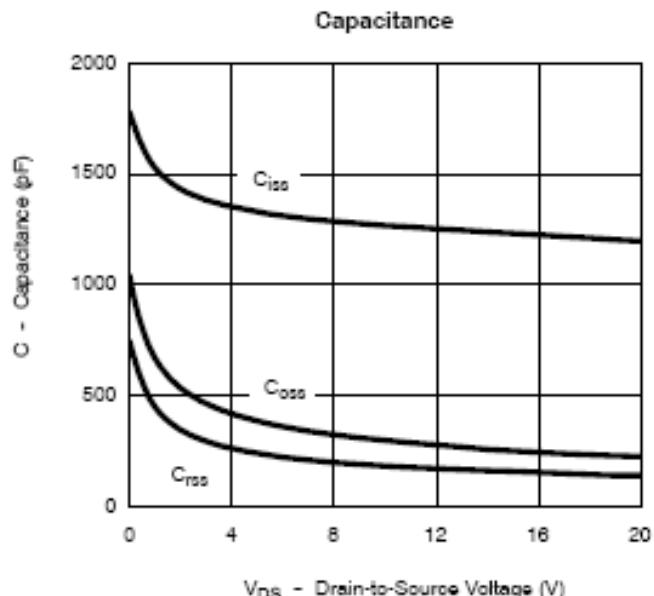
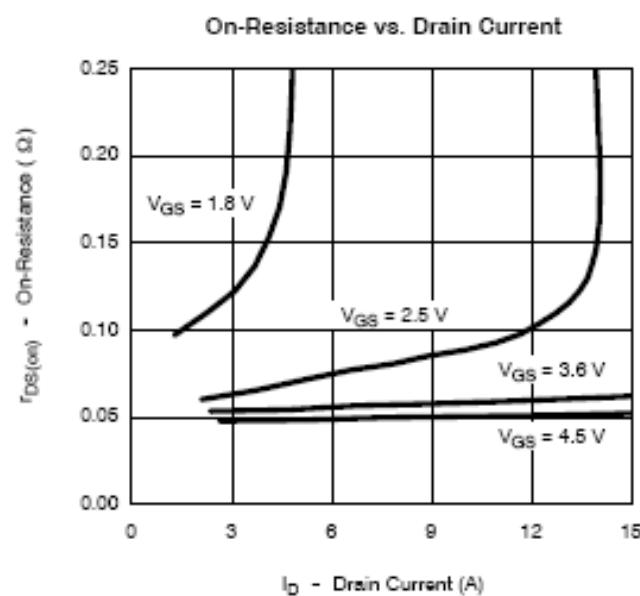
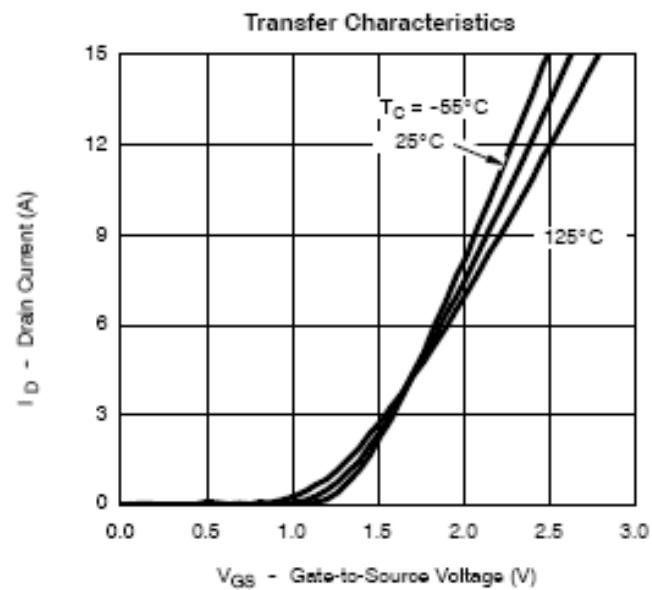
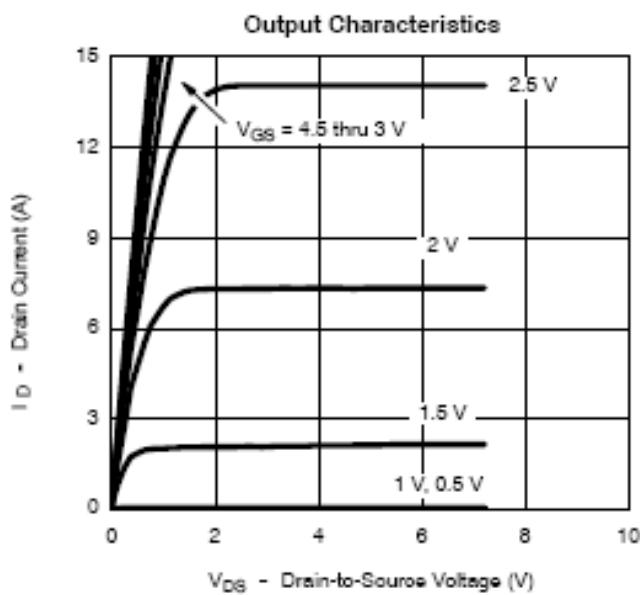
(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=-250uA	-20			V
Gate Threshold Voltage	V _{GS(th)}	V _{Ds} =V _{GS} , ID=-250uA	-0.32		-0.8	
Gate Leakage Current	I _{GSS}	V _{Ds} =0V, V _{GS} =±12V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{Ds} =-20V, V _{GS} =0V			-1	uA
		V _{Ds} =-20V, V _{GS} =0V T _J =55°C			-5	
Drain-Source On-Resistance	R _{Ds(on)}	V _{GS} =-4.5V, ID=-6.0A		0.055	0.065	Ω
		V _{GS} =-2.5V, ID=-3.6A		0.072	0.085	
		V _{GS} =-1.8V, ID=-2.0A		0.092	0.105	
Forward Transconductance	g _{fs}	V _{Ds} =-5V, ID=-2.8A		6		S
Diode Forward Voltage	V _{SD}	I _S =-6A, V _{GS} =0V		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{Ds} =-10V, V _{GS} =-4.5V ID=-8.0A		4.8	8	nC
Gate-Source Charge	Q _{gs}			1.0		
Gate-Drain Charge	Q _{gd}			1.0		
Input Capacitance	C _{iss}	V _{Ds} =-10V, V _{GS} =0V f=1MHz		485		pF
Output Capacitance	C _{oss}			85		
Reverse Transfer Capacitance	C _{rss}			40		
Turn-On Time	t _{d(on)}	V _{DD} =-10V, R _L =6Ω ID=-1.0A, V _{GEN} =-4.5V R _G =6Ω		10	16	ns
	t _r			13	23	
Turn-Off Time	t _{d(off)}			18	25	
	t _f			15	20	



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TYPICAL CHARACTERISTICS

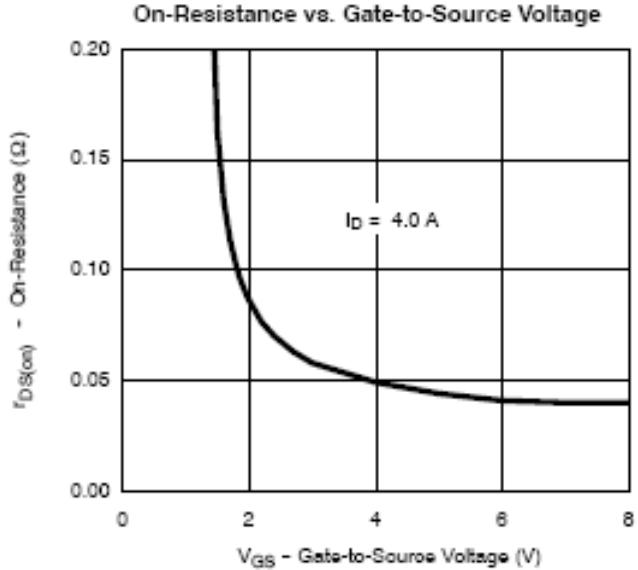
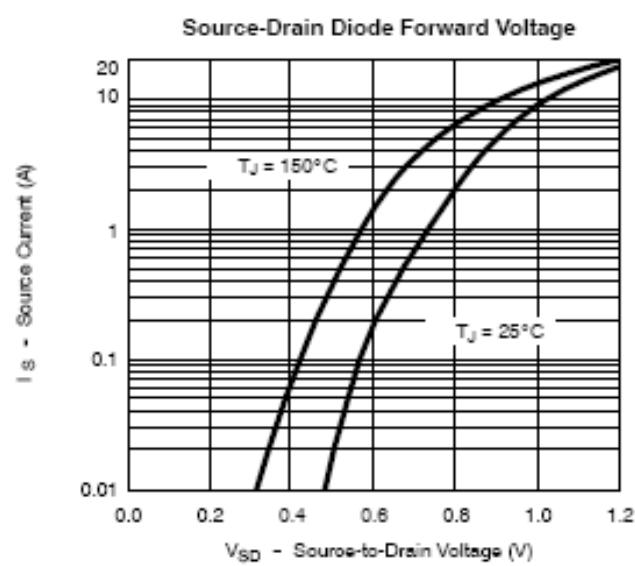
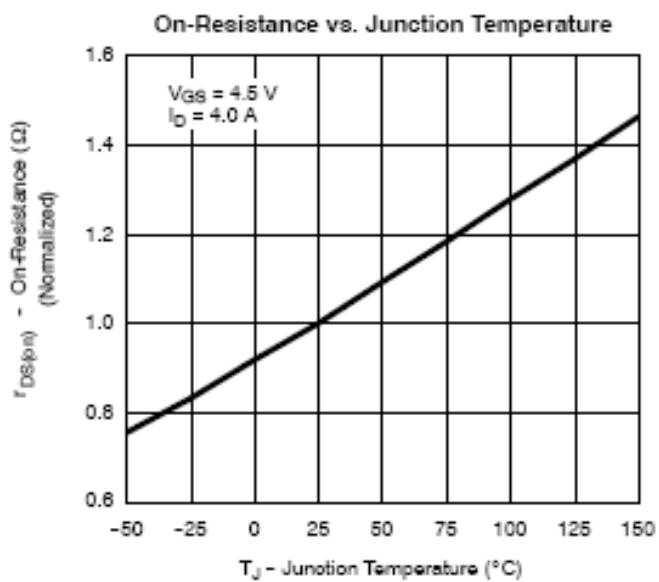
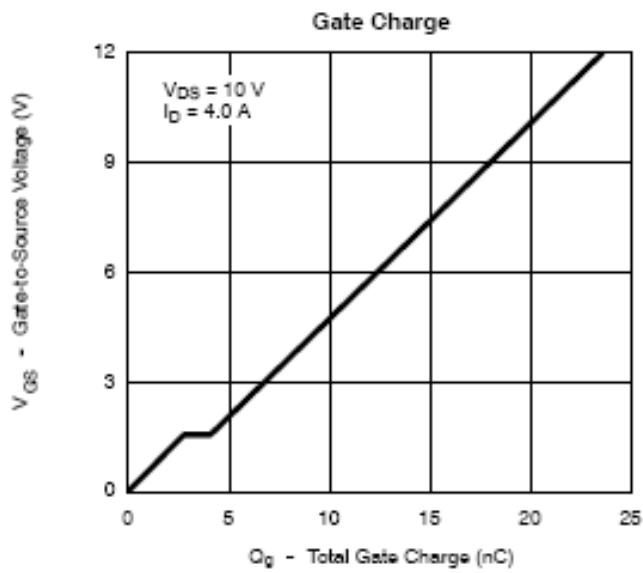




SPP2095

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TYPICAL CHARACTERISTICS

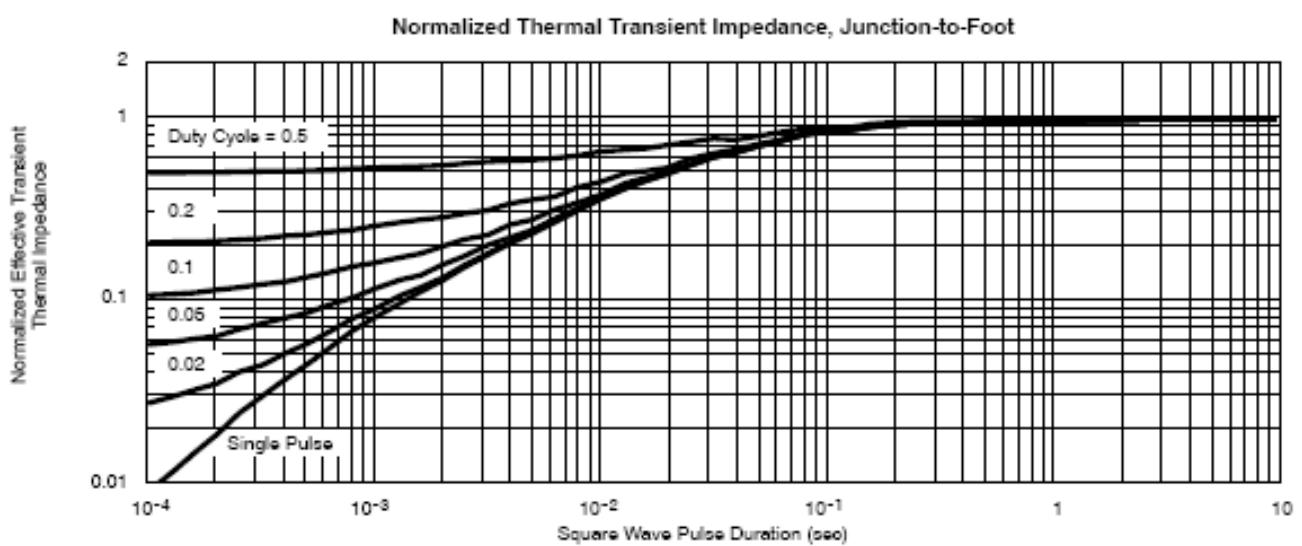
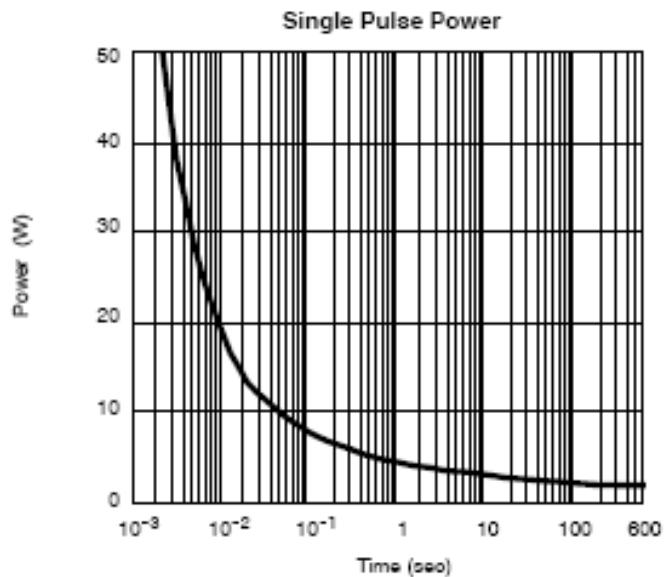
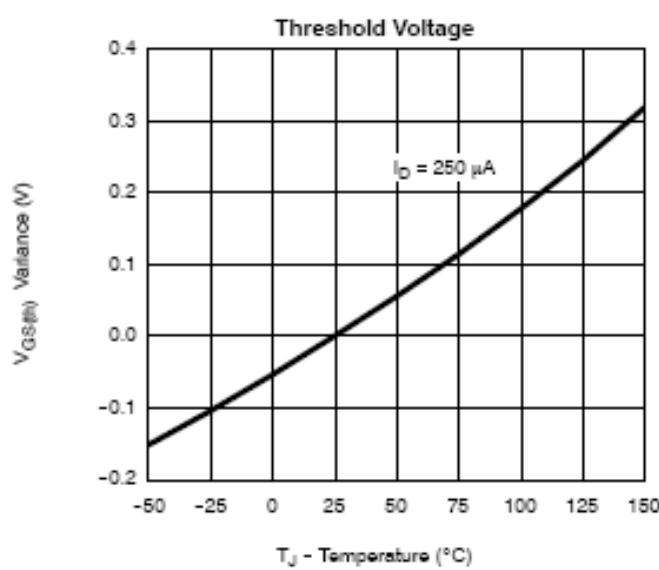




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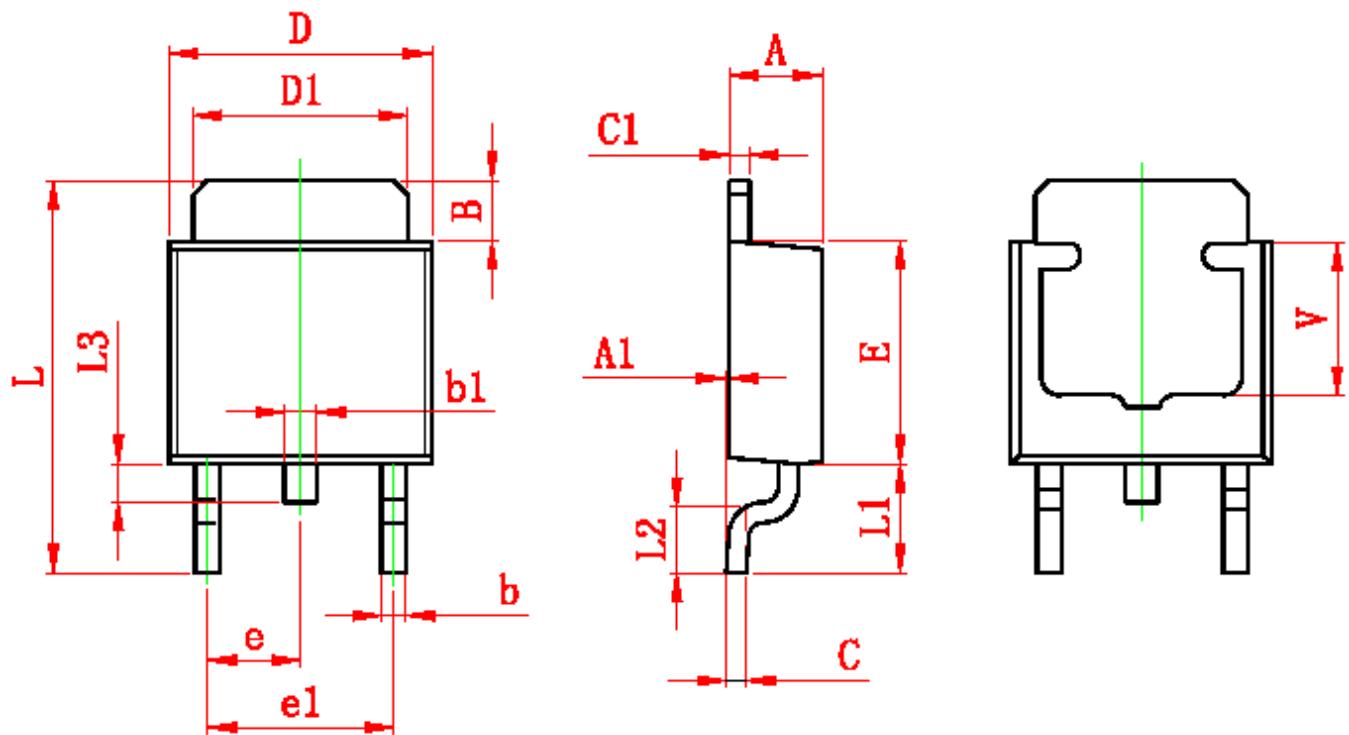




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TO-252-2L PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	



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